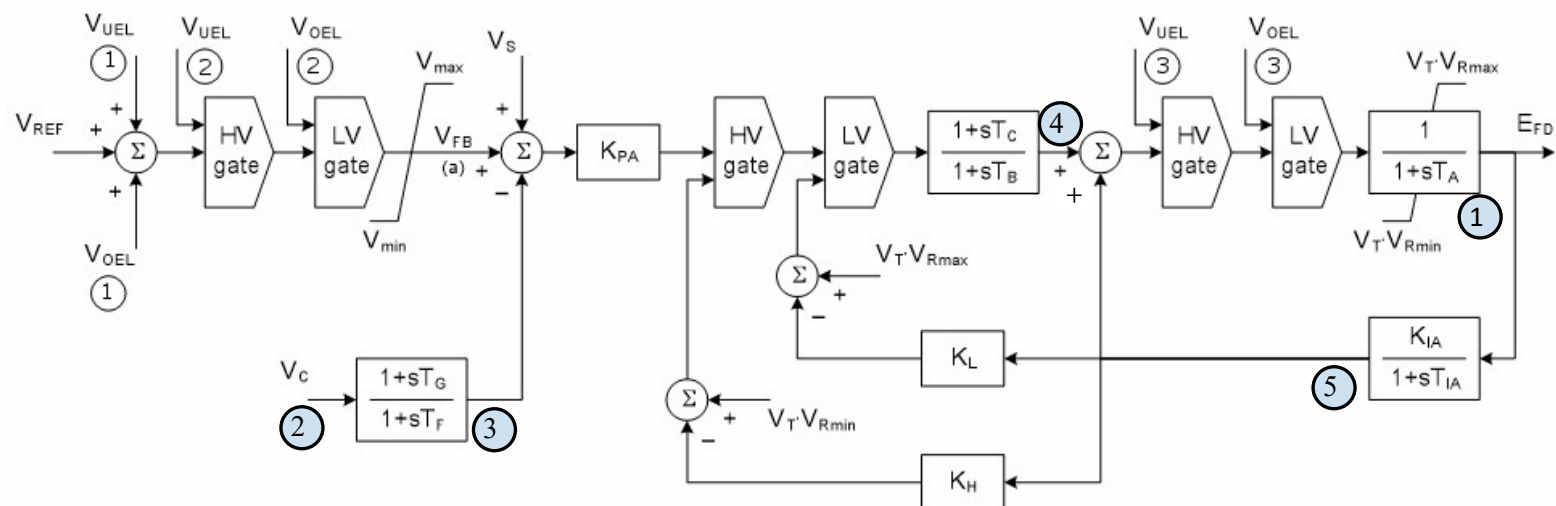


Exciter ST7C

Exciter ST7C IEEE Std 421.5-2016 Exciter with static potential source



States:

- 1 – E_{FD}
- 2 – Sensed V_T
- 3 – InputLL
- 4 – LL2
- 5 – Feedback

alternate UEL input locations (V_{UEL})

- ① summation point (voltage reference setpoint)
- ② take-over (at voltage reference)
- ③ take-over (at voltage regulator output)

alternate SCL input locations (V_{SCL})

- ① summation point (voltage reference setpoint)
- ② take-over (at voltage reference)
- ③ take-over (at voltage regulator output)

alternate OEL input locations (V_{OEL})

- ① summation point (voltage reference setpoint)
- ② take-over (at voltage reference)
- ③ take-over (at voltage regulator output)